

FIG. 1A
(PRIOR ART)

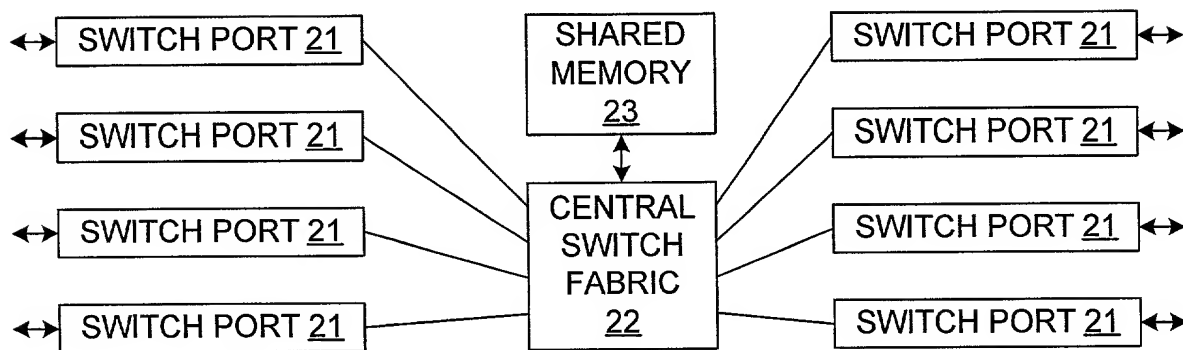


FIG. 1B
(PRIOR ART)

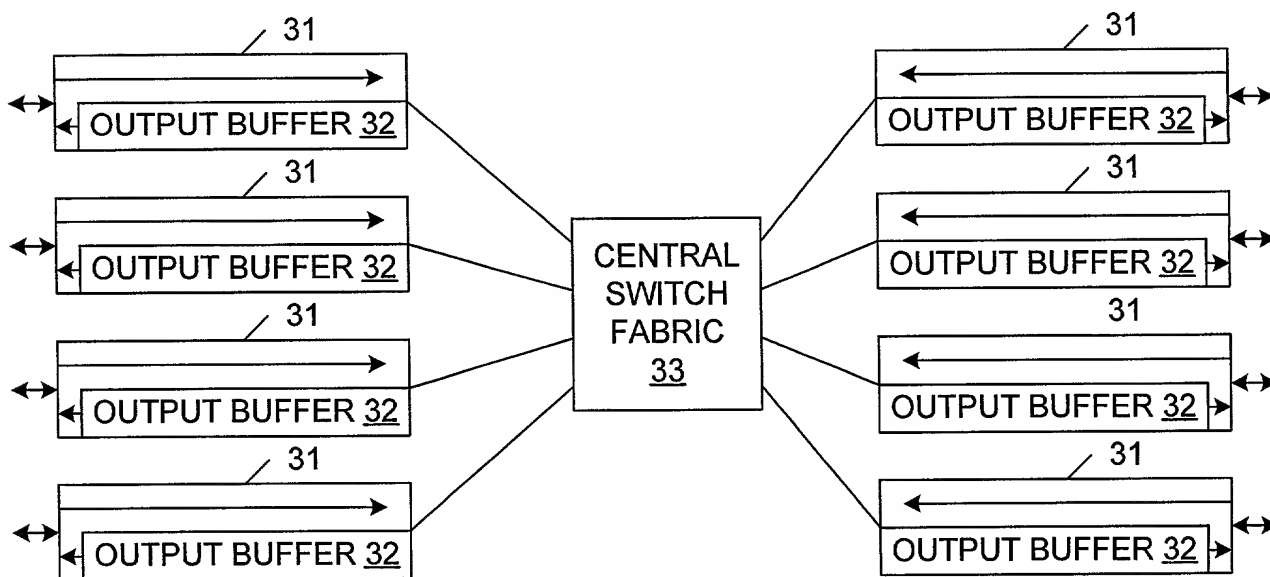


FIG. 1C
(PRIOR ART)

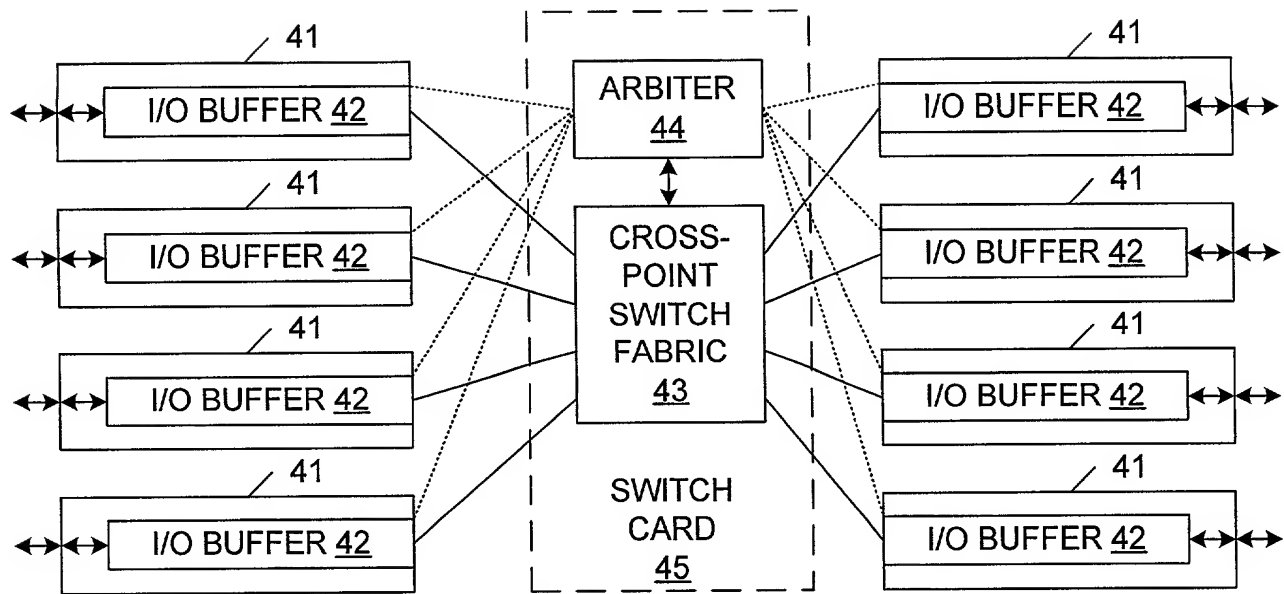


FIG. 1D
(PRIOR ART)

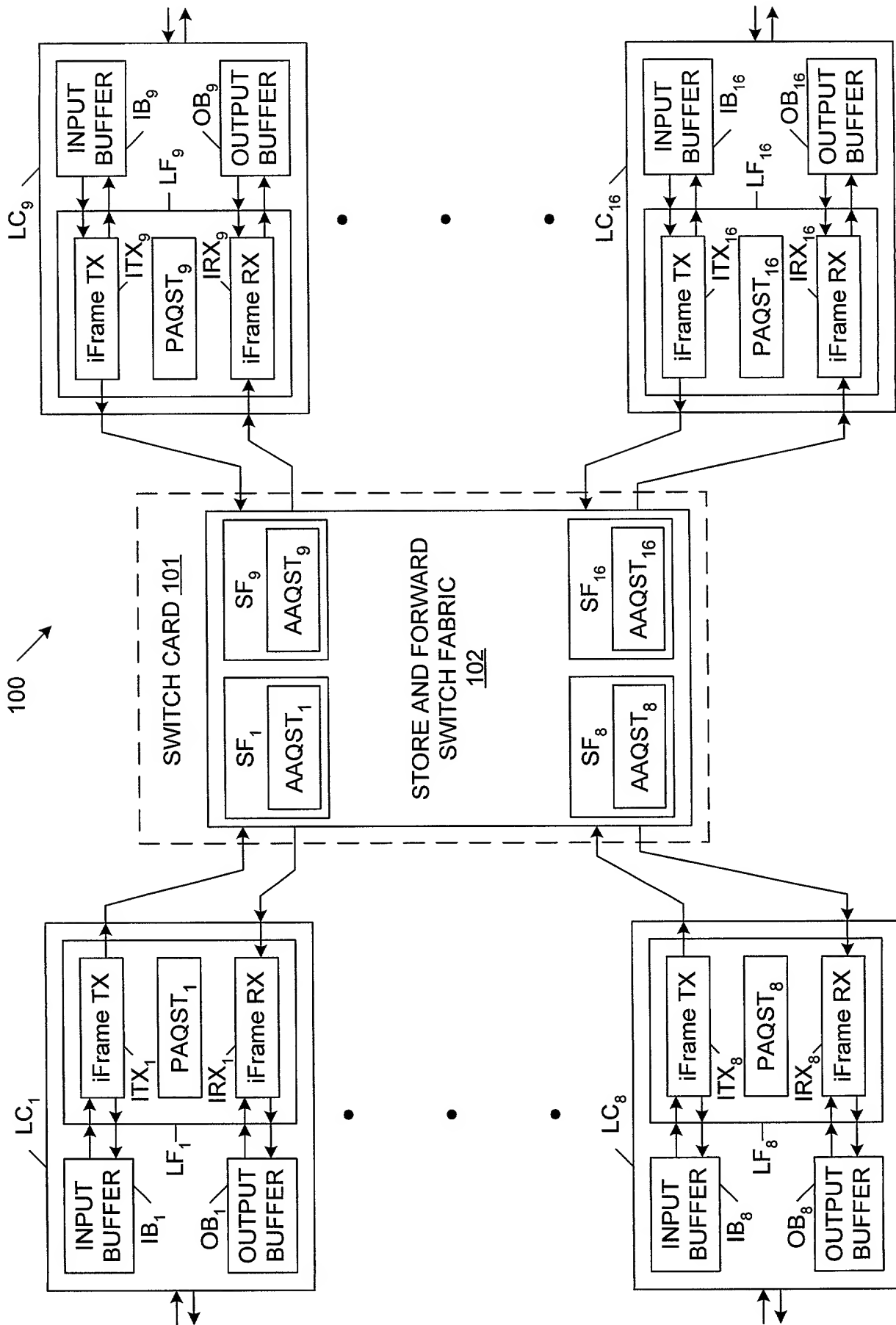


FIG. 2

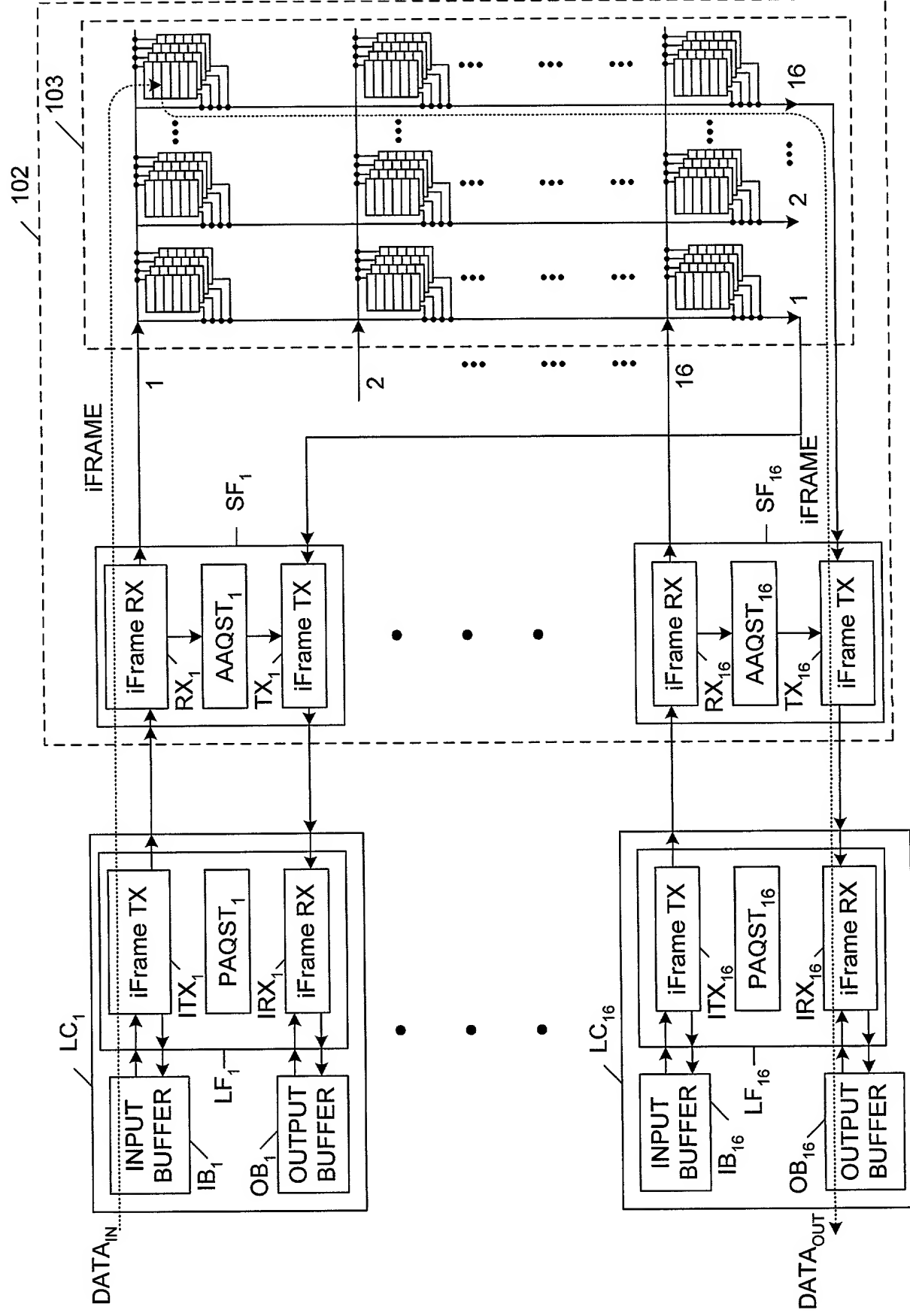


FIG. 3

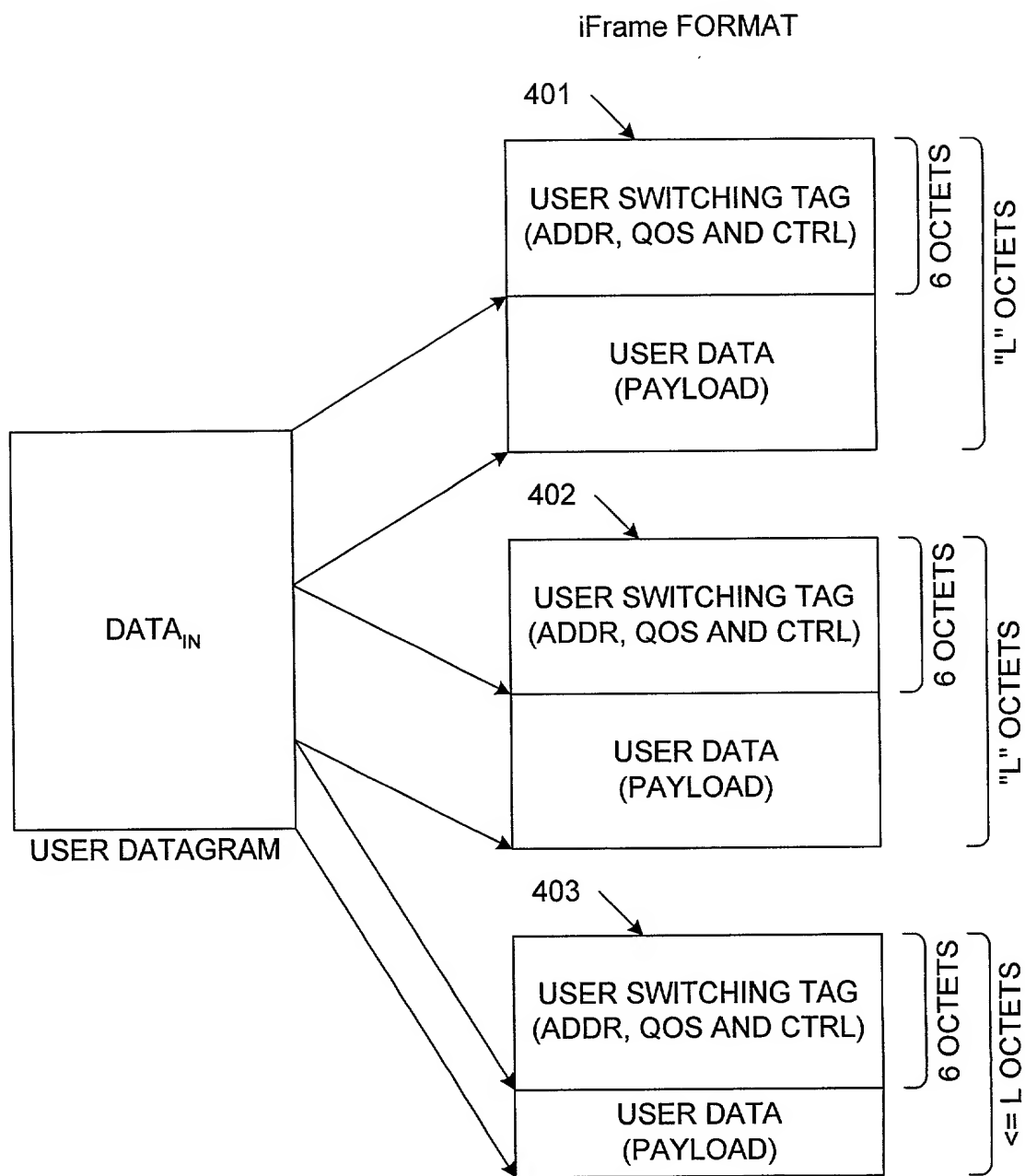


FIG. 4

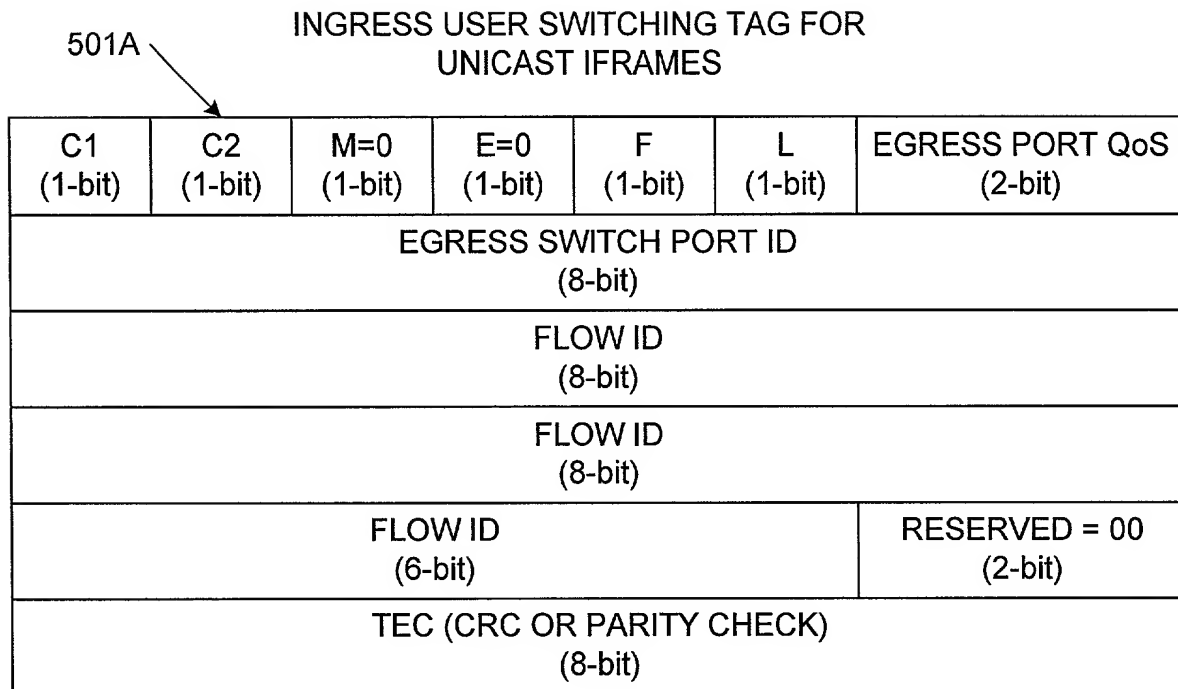


FIG. 5A

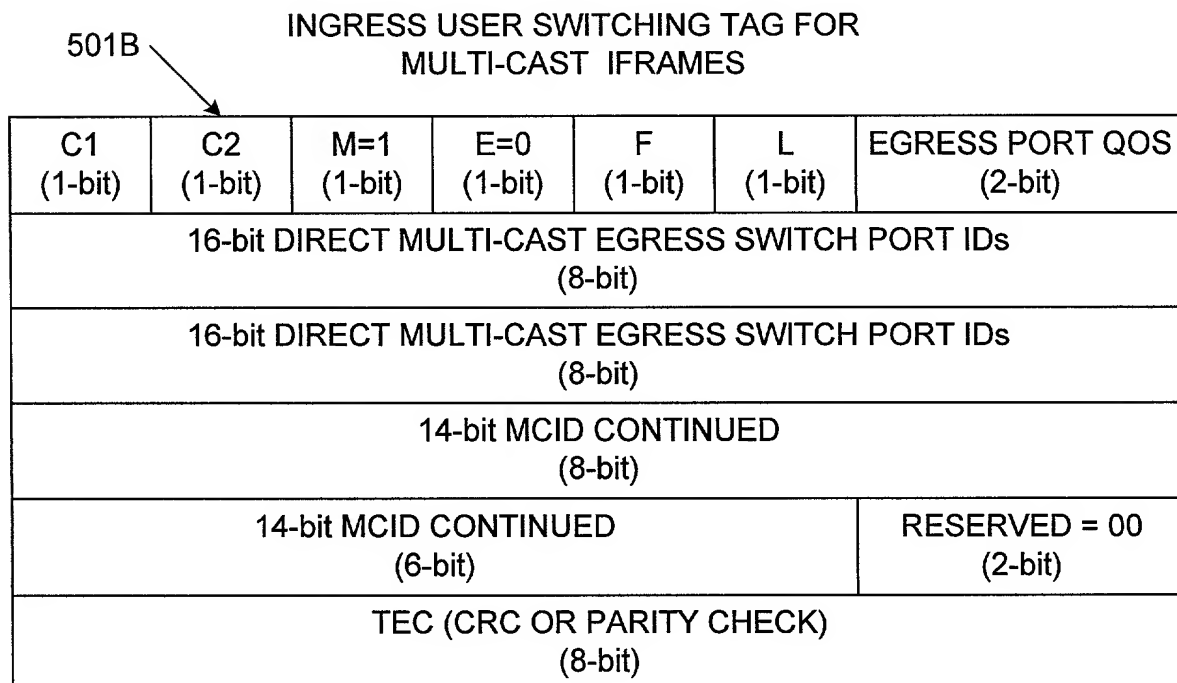


FIG. 5B

EGRESS USER SWITCHING TAG FOR UNICAST IFRAMES

601A

C1 (1-bit)	C2 (1-bit)	M=0 (1-bit)	E=0 (1-bit)	F (1-bit)	L (1-bit)	PQS UPDATE QOS (2-bit)
PQS UPDATE ID (8-bit)						
22-bit FLOW ID (8-bit)						
22-bit FLOW ID (8-bit)						
22-bit FLOW ID (6-bit)						RESERVED = 00 (2-bit)
TEC (CRC OR PARITY CHECK) (8-bit)						

FIG. 6A

EGRESS USER SWITCHING TAG FOR MULTI-CAST IFRAMES

601B

C1 (1-bit)	C2 (1-bit)	M=1 (1-bit)	E=0 (1-bit)	F (1-bit)	L (1-bit)	PQS UPDATE QOS (2-bit)
PQS UPDATE ID (8-bit)						
RESERVED = 0000 0000 (8-bits)						
14-bit MCID CONTINUED (8-bit)						
14-bit MCID CONTINUED (6-bit)						RESERVED = 00 (2-bit)
TEC (CRC OR PARITY CHECK) (8-bit)						

FIG. 6B

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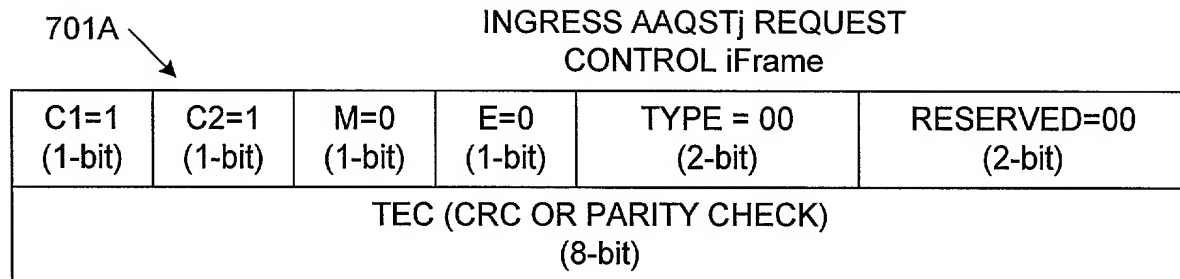


FIG. 7A

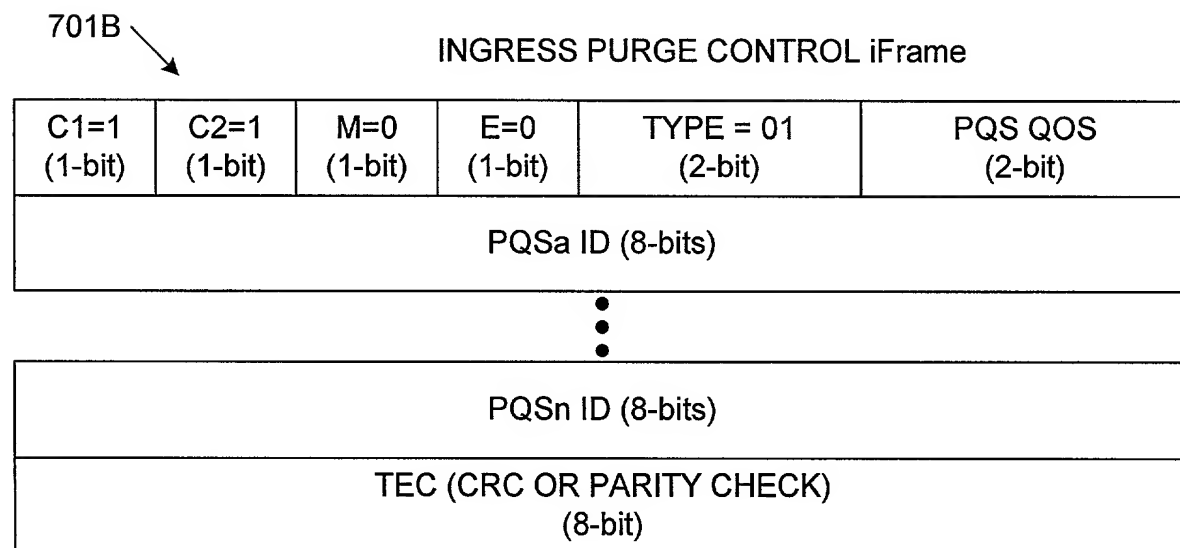


FIG. 7B

801A

AAQST_J TABLE UPDATE

C1=1 (1-bit)	C2=1 (1-bit)	M=0 (1-bit)	E=0 (1-bit)	F=0 (1-bit)	L=0 (1-bit)	TYPE=00 (2-bit)
4-bit AQS _{J,1,1} UPDATE INCREMENT				4-bit AQS _{J,1,2} UPDATE INCREMENT		
4-bit AQS _{J,1,3} UPDATE INCREMENT				4-bit AQS _{J,1,4} UPDATE INCREMENT		
4-bit AQS _{J,2,1} UPDATE INCREMENT				4-bit AQS _{J,2,2} UPDATE INCREMENT		
4-bit AQS _{J,2,3} UPDATE INCREMENT				4-bit AQS _{J,2,4} UPDATE INCREMENT		
4-bit AQS _{J,15,1} UPDATE INCREMENT				4-bit AQS _{J,15,2} UPDATE INCREMENT		
4-bit AQS _{J,15,3} UPDATE INCREMENT				4-bit AQS _{J,15,4} UPDATE INCREMENT		
4-bit AQS _{J,16,1} UPDATE INCREMENT				4-bit AQS _{J,16,2} UPDATE INCREMENT		
4-bit AQS _{J,16,3} UPDATE INCREMENT				4-bit AQS _{J,16,4} UPDATE INCREMENT		
TEC (CRC OR PARITY CHECK) (8-bit)						

FIG. 8A

EGRESS PQS UPDATE CONTROL
iFrame

801B

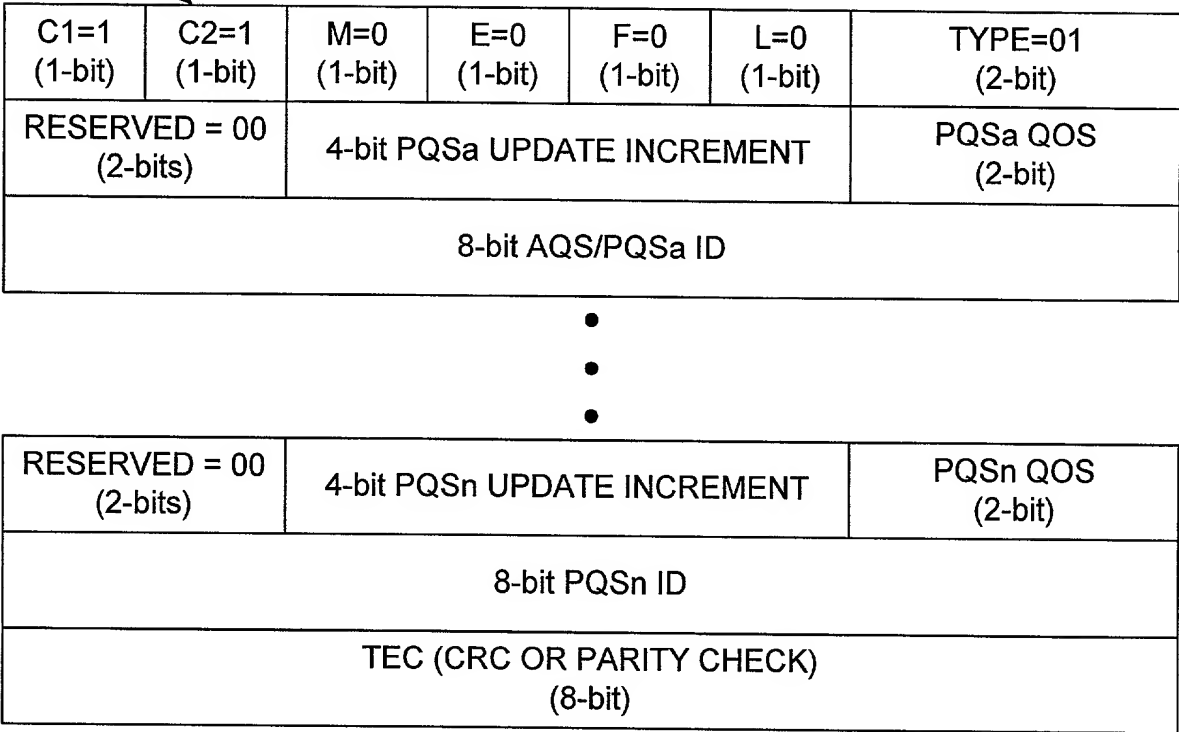


FIG. 8B

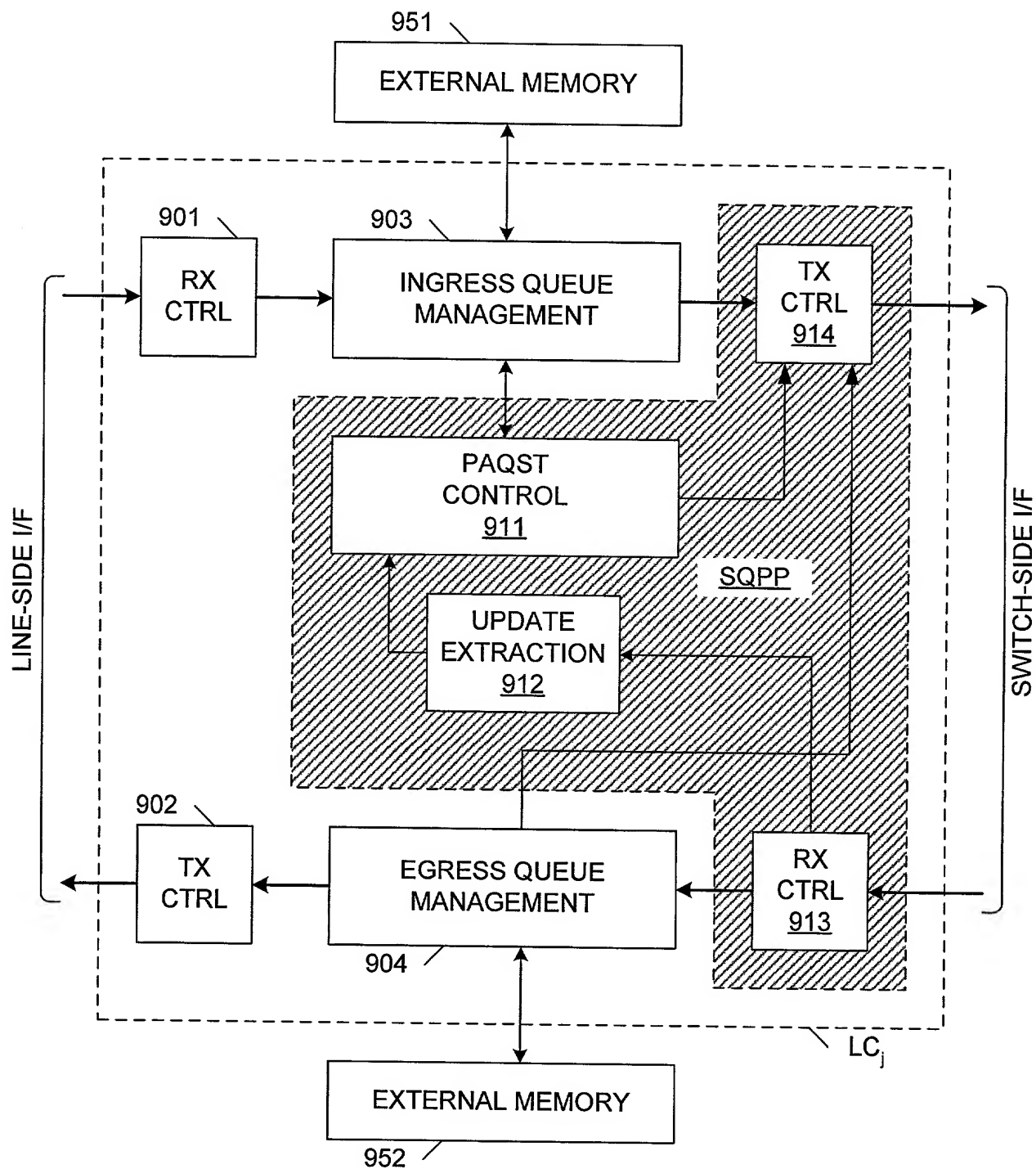


FIG. 9

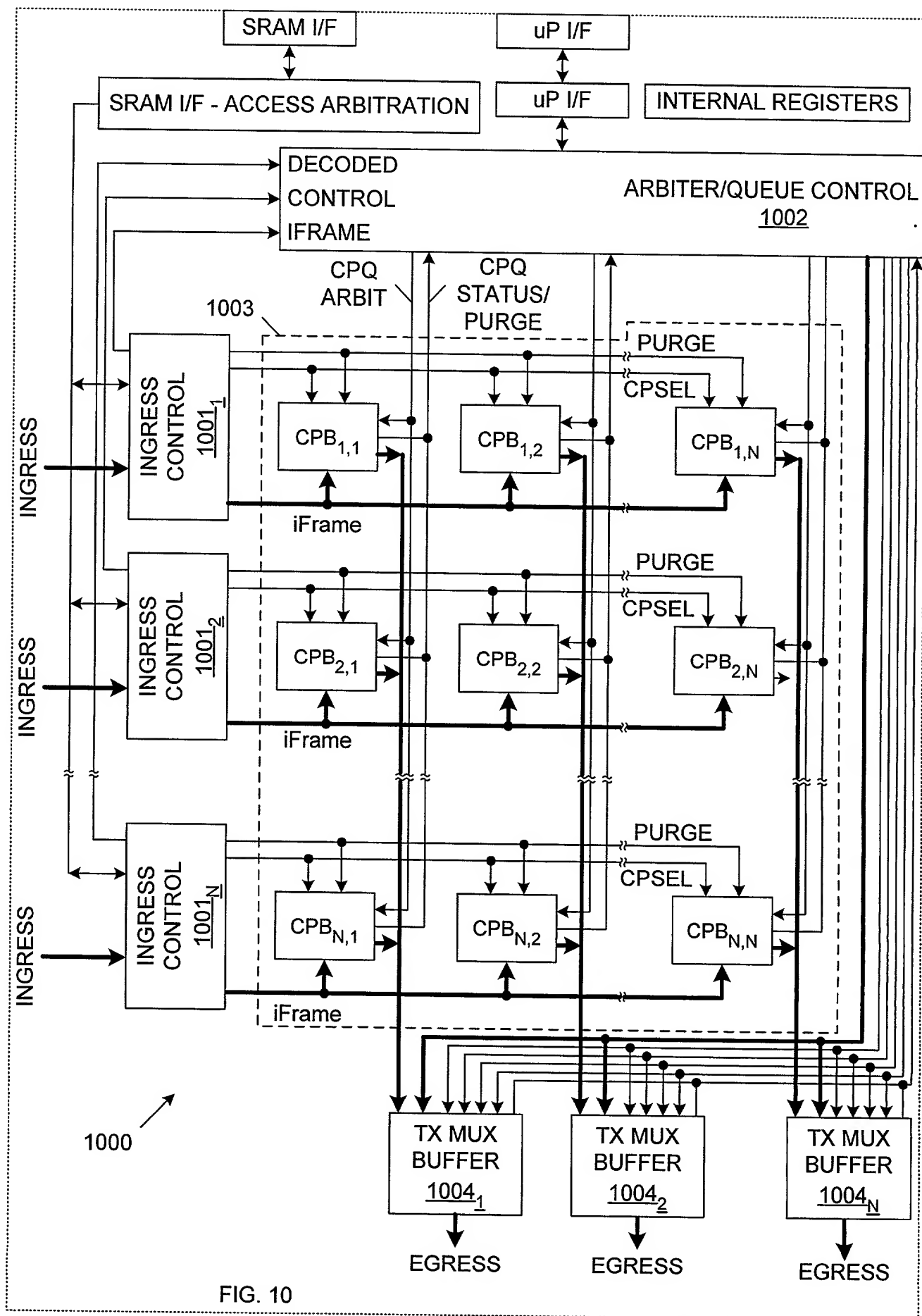


FIG. 10